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<b>SERIAL NUMBER</b> 10/817,004	<b>FILING OR 371(c) DATE</b> 04/05/2004 <b>RULE</b>	<b>CLASS</b> 257	<b>GROUP ART UNIT</b> 2826	<b>ATTORNEY DOCKET NO.</b> HITA.0536
<b>APPLICANTS</b> Naoki Yamamoto, Kawaguchi, JAPAN; Yoshitaka Tadaki, Hanno, JAPAN; Hiroshige Kogayu, Fussa, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2003-110997 04/16/2003				
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> ** 06/17/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <u>Examiner's Signature</u> Initials		<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 22	<b>TOTAL CLAIMS</b> 16
<b>INDEPENDENT CLAIMS</b> 2				
<b>ADDRESS</b> Stanley P. Fisher Reed Smith LLP Suite 1400 3110 Fairview Park Drive Falls Church, VA22042-4503				
<b>TITLE</b> Semiconductor integrated circuit device and manufacturing method thereof				
<b>FILING FEE RECEIVED</b> 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	